

CONSOCIA
ADVISORY

Communications & PR Review

October 2024



HIGHLIGHTS

9

Total Number of
Stories published (1st
Oct to 25th Oct)

3+1_(RBM)

OCTOBER
(including PR+ Media
Interview)

7

OCTOBER
(excluding PR)

1

TOTAL PRINT (1st Oct
to 25th Oct)

8

TOTAL ONLINE(1st Oct
to 25th Oct)

0

Press releases issued

TRADITIONAL PR

1,024

Reactions

41

Comments

8

Reposts

LinkedIn

(01st October to 22nd
October)

61,579

Impressions

4,862

Clicks

Twitter

(01st October to 24th October)

1,279

Impressions

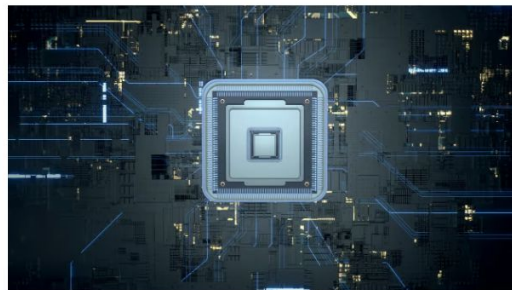
59

Engagement

India ranks 3rd in global semicon backend index

While Taiwan leads the index, followed by Malaysia, Mainland China ranks fourth, Korea ninth, Vietnam tenth, Singapore 12th, and Japan 13th.

By NIDHI SINGAL, Oct 23, 2024 | 8 min read



Thirty countries and regions were evaluated on the attractiveness of locations for back-end semiconductor manufacturing.

“India’s strong ranking is driven by factors such as ease of doing business, government incentives, supportive labour laws, a growing talent pool and the availability of willing Indian JV partners. Additionally, India’s rapidly increasing demand for semiconductors also enhances its attractiveness as a key player in the global market,” says Ashok Chandak, President, India Electronics & Semiconductor Association (IESA).

ET Government.com
Digital India
Leadership Series

Dr. V Veerapan
Chairperson, India Electronics & Semiconductor Association
Co-founder, Tessolve Semiconductor Pvt Ltd

Anoop Verma
Editor - News
ET Government

ET Government

[Music]

THE FREE PRESS JOURNAL

Chip market to surpass \$100 bn in revenues by 2030

PTI
NEW DELHI

India’s semiconductor market is set to cross \$100 billion in revenues by 2030, driven by government initiatives like PLI scheme amid robust demand to build digital infrastructure across industries, according to a report on Wednesday. Mobile handset, IT and telecom segments are leading the chip industry, contributing more than 75 per cent of its revenues, according to a report by the IESA and Counterpoint Research.

Semiconductor Dream Gets Real

India’s top companies are making bold moves in the semiconductor space, seizing the momentum of the global “chip gold rush.”

By NIDHI SINGAL, Oct 7, 2024 | 10 min read



SITUATED 124 KILOMETRES away from Ahmedabad’s Sardar Vallabhbhai Patel International Airport, the town of Dholera began attracting real estate developers in March this year. At that time, the landscape was mostly barren, marked by a few structures such as the Dholera office and the ReNew solar cell plant. Despite the sparse surroundings, developers were travelling from Ahmedabad with potential buyers, drawn by the promise of future townships complete with schools, hospitals, and even an airport.

What sparked this sudden wave of interest? The catalyst was Tata Electronics’ plan to build a mega semiconductor fabrication plant with an investment of ₹91,000 crore, expected to create over 20,000 direct and indirect jobs in the region. Unlike the verticals Tata Group companies have traditionally operated in, this marks the conglomerate’s foray into the global semiconductor ecosystem.

With up to 70% capex subsidies offered by governments, the semiconductor sector presents a lucrative investment opportunity, which explains why groups such as Hiranandani are interested in the sector. “It has the capital to invest, and because of the schemes, it’s a good time to begin,” explains V. Veerappan, chairman, Indian Electronics and Semiconductor Association.

Zoho’s CEO and founder, Sridhar Vembu, on the other hand, is committed to creating products in India that serve global markets. Vembu views semiconductors as a crucial technology for the nation and advocates investment in industrial R&D. “Zoho is going for a silicon carbide fab, which requires lower initial investment and is more feasible compared to high-capital logic fabs,” says Veerappan.

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At IMC2024, our CEO, [Himamshu Khasnis](#), showed Hon. Prime Minister of India Shri [Narendra Modi](#)ji and Union Minister of Communications Shri [Jyotiraditya M Scindia](#)ji our 4G/5G/NavIC Chips and RAN systems that are being created at [Signalchip](#) and [Signaltron](#). They were really appreciative of our efforts towards our shared goal of Aatmanirbhar Bharat.

Additionally, we would like to thank [Department of Telecommunications \(DOT \)](#) for their invaluable support along this journey!

#IMC2024 #makeinindia #startupindia #5G

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FREE

Bobby Mitra appointed CIO of Tata Electronics

Updated: October 01, 2024 at 12:28 PM

Mitra and his team will be instrumental in advancing Tata Electronics' commitment to AI-led manufacturing excellence

By [B L NARAYAN BUREAU](#)

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In this role, Mitra will drive enterprise-wide initiatives in AI and digital transformation for Electronic Manufacturing Services, Semiconductor Assembly and Test, Semiconductor Foundry, and Design Services businesses.

Mitra and his team will be instrumental in advancing Tata Electronics' commitment to AI-led manufacturing excellence, particularly in establishing India's first semiconductor fab in Dhulela, Gujarat and the first indigenous semiconductor assembly and test facility in Jajiroad, Assam.



Design Linked Incentive (DLI)

invites project proposals from Chip Designers across the country for the following categories:

Semiconductor Products (Chips)

<ul style="list-style-type: none"> • Mobile SoC • Wearable SoC • Tiny Microcontroller • Micro Controller • Mid-Range Microprocessor • High End Microprocessor • Server Processor • AI Accelerator Chip • GRU Chip • Image/Video Proc with AI • Neuromorphic AI Chip • Audio Processor Chip • WiFi Chip 6E • 4G/5G/6G Modem (Consumer) • BT/LE 5.0/6.0 	<ul style="list-style-type: none"> • NB/IoT Chip • NFC Chip • GPS/NAVIC Chip • 5G RF Chip • Radar RF Chip • Lidar Chip • Radar Chip for Automotive • Radar Chip for Radar • ONT Chip with x-GPON • Network Processor • LED Driver Chip • GaN Power Chip • SiC Power Chip • DSP Chip • Sensors of Various types
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*Most of the chips should be design as standalone or in Chiplet form

Submit Your Proposals

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pmu@cdac.in
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